EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		display and (shift adj register) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND same (consist$6 same nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 15:46
L2	0	"6,686,899".pn. and hold$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 12:11
L3	1	"6,670,944".pn. and hold$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 12:11
L4	8	Kazuhiro near3 NODA and display and shift adj register and hold$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 12:28
L5	2	345/100.CCLS. AND display and (shift adj register) and hold$6 adj circuit$6 and clock$6 and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 12:48
L6	3	display and (shift adj register) and (hold$6 adj circuit$6) same CMOS same inverter and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 12:49
L7	3	display and (shift adj register) and (hold$6 adj circuit$6) same CMOS same inverter and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 13:02
L8	0	display and (shift adj register) and (hold$6 adj circuit$6) same CMOS same inverter and clock$6 and wave$6 same shape$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 13:03
L9	14	display and (shift adj register) and (hold$6 same circuit$6) same CMOS same inverter and clock$6 and wave$6 same shape$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 13:04

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S1	96	display and shift adj register same consist$6 same nand	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:40
S2	73	display and shift adj register same consist$6 same nand same circuit$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:46
S3	3	display and shift adj register same consist$6 same nand same circuit$6 and NMOS and PMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:41
S4	3	display and shift adj register same consist$6 same nand same circuit$6 and NMOS and PMOS and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:41
S5	0	display and shift adj register same consist$6 same nand same circuit$6 and NMOS and PMOS and clock and power same supply$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:42
S6	0	display and shift adj register same consist$6 same nand same circuit$6 and NMOS and PMOS and clock and power$6 same supply$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:42
S7	0	display and (shift$6 adj register$6) same consist$6 same nand same circuit$6 and NMOS and PMOS and clock$6 and power$6 same supply$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:43
S8	0	display and (shift$6 same register$6) same consist$6 same nand same circuit$6 and NMOS and PMOS and clock$6 and power$6 same supply$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:44
S9	3	display and (shift$6 same register$6) same consist$6 same nand same circuit$6 and NMOS and PMOS and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:43
S10	0	display and (shift$6 same register$6) same consist$6 same nand same circuit$6 and NMOS and PMOS and clock$6 and power$6 and supply$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:44

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S11	3	display and (shift$6 same register$6) same consist$6 same nand same circuit$6 and NMOS and PMOS and clock$6 and power$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:44
S12	22	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:52
S13	585	Kazuhiro and NODA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:47
S14	107	Kazuhiro and NODA and display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:47
S15	96536	Kazuhiro nera3 NODA and display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:47
S16	73	Kazuhiro near3 NODA and display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:48
S17	9	Kazuhiro near3 NODA and display and shift adj register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 12:28
S18	3	Kazuhiro near3 NODA and display and shift adj register and nand	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:49
S19	9	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:54
S20	0	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6 and NMOS and PMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:52

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S22	0	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and PMOS and NMOS and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:54
S23	3	display and (shift adj register) same (consist$6 same nand same circuit$6) and PMOS and NMOS and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:54
S24	9	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6 and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 13:16
S25	3	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6 and clock$6 and odd and even	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 13:19
S26	2	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6 and clock$6 and odd and even and phase$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 15:41
S27	2	"20040164947"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 15:43
S28	1	"20040164947" and parallel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 15:18
S29	0	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 15:45
S30	1	"20040164947" and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 15:44
S31	28	display and (shift adj register) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 15:45

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S32	0	display and (shift adj register) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (consist$6 same nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 15:46
S33	28	display and (shift adj register) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 15:46
S34	5	display and (shift adj register) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 15:46
S35	50	Tachibana AND Toshikazu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:27
S36	64	Tachibana AND Toshikazu display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:29
S37	1	Tachibana AND Toshikazu AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:30
S38	1	Sakamaki AND Goro AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:30
S39	1	Endo AND Kazuya AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:30
S40	1	Iwasaki AND Yoshitaka AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:30
S41	2	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:31

S42	8	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 12:44
S43	0	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 and wave same shap$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:12
S44	14	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and wave same shap$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:16
S45	0	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and wave same shap$6 and nand same cicuit$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:12
S46	9	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and wave same shap$6 and nand same circuit$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:13
S47	7	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and wave adj shap$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:35
S48	0	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and rise adj time adj improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:35
S49	0	display and (shift adj register) and hold$6 and clock$6 and rise adj time adj improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:36
S50	0	display and (shift adj register) and hold$6 and clock$6 and rise$6 adj time adj improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:36
S51	5	display and (shift adj register) and rise$6 adj time adj improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:36